

PRINTER RUSH
(PTO ASSISTANCE)

Application : 09/311,092 Examiner : Nguyen GAU : 2675
From : MR Location : IDC FMF FDC Date : 01-17-06

Tracking # : EPM09311092 Week Date : 11-28-05

DOC CODE	DOC DATE	MISCELLANEOUS
<input checked="" type="checkbox"/> 1449	<u>04-10-02</u>	<input type="checkbox"/> Continuing Data
<input type="checkbox"/> IDS		<input type="checkbox"/> Foreign Priority
<input type="checkbox"/> CLM		<input type="checkbox"/> Document Legibility
<input type="checkbox"/> IIFW		<input type="checkbox"/> Fees
<input type="checkbox"/> SRFW		<input type="checkbox"/> Other
<input type="checkbox"/> DRW		
<input type="checkbox"/> OATH		
<input type="checkbox"/> 312		
<input type="checkbox"/> SPEC		

[RUSH] MESSAGE: The reference on other prior art section line 1
on page 3 of PTO 1449 dated 04-10-02 contains dates
that is covered by hole markings.
please resolve.

Thank you,
MR

[XRUSH] RESPONSE:

Dhe

INITIALS: MR

NOTE: This form will be included as part of the official USPTO record, with the Response document coded as XRUSH.

REV 10/04

"CMOS Wireless

Kluwer Academic

AS	Crols et al., "CMOS Wireless Transceiver Design", Kluwer Academic Publishers, 1997, pp. 17-23.
AT	Chang et al., "A CMOS Channel-Select Filter for a Direct-Conversion Wireless Receiver", to appear in <u>IEEE Journal of Solid-State Circuits</u> , April 1999.
AU	Asad A. Abidi, "Direct-Conversion Radio Transceivers for Digital Communications", <u>IEEE Journal of Solid-State Circuits</u> , Vol. 30, No. 12, December 1995, pp. 1399-1410.
AV	Behzad Razavi, "Design Considerations for Direct-Conversion Receivers", <u>IEEE Transactions on Circuits and Systems--II: Analog and Digital Signal Processing</u> , Vol. 44, No. 6, June 1997, pp. 428-435.
AW	Thomas H. Lee, "The Design of COMS Radio-Frequency Integrated Circuits", Cambridge University Press, 1998, pp. 344-351.
AX	Product Specification for Advanced Pager Receiver UAA2082, Integrated Circuits, January 16, 1996.
AY	Moulding et al., "Gyrator Video Filter IC with Automatic Tuning", <u>IEEE Journal of Solid-State Circuits</u> , Vol. SC15, No. 6, December 1980, pp. 963-968.

EXAMINER:

DATE CONSIDERED:

3/28/02

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



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